U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
DJH	1	5,493,508	2/20/96	Dangelo, et al.			
DJH	2	5,535,331	7/9/96	Swoboda et al.			
DJH	3	5,544,067	8/6/96	Rostoker, et al.			
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	TRANSLATION NO
DJH	17	2 308 470	6/97	GB				
DJH	18	WO 97 13209 A	04/1997	WIPO				

EXAMINER INITIAL		OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
DJH	19	Elms, A., "Tuning a Customisable RISC Core for DSP," Electronic Product Design, Sept. 1997, Vol. 18, No. 9, pages 19-20, 22, XP000909039
DJH	20	Tensilica, Inc; Xtensa Instruction Set Architecture (ISA) Reference Manual Revision 1.0. (First 25 pages)
DJH	21	Tensilica, Inc. Instruction Extension Language (TIE) Reference Manual Revision 1.3.
DJH	22	Compton et al., "Configurable Computing: A Survey of Systems and Software," Technical Report, Northwestern University, Dept. of ECE, 1999.
DJH	23	Yang, Jin-Hyuk, et al., "MetaCore: A Configurable & Instruction-Level Extensible DSP Core," Proceedings of the ASP-DAC '98 Asian and South Pacific Design Automation Conference 1998, pages 325-326, XP-002137268

EXAMINER	David J. Newman	DATE CONSIDERED	9-31-15

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